

WEST Search History

DATE: Thursday, September 15, 2005

Hide? Set Name Query**Hit Count***DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR*

| | | | |
|--------------------------|-----|--|------|
| <input type="checkbox"/> | L12 | L11 and cycle | 6 |
| <input type="checkbox"/> | L11 | L8 and port | 9 |
| <input type="checkbox"/> | L10 | L8 and (port same (input/output or i/o)) | 3 |
| <input type="checkbox"/> | L9 | L8 and ((i/o or input/output) same register) | 5 |
| <input type="checkbox"/> | L8 | L7 and register | 9 |
| <input type="checkbox"/> | L7 | L6 and (data same transfer\$4) | 12 |
| <input type="checkbox"/> | L6 | L5 and flag | 21 |
| <input type="checkbox"/> | L5 | l3 and (clock same (pin or port)) | 82 |
| <input type="checkbox"/> | L4 | L3 and (clock adj pin) | 2 |
| <input type="checkbox"/> | L3 | (kvm or ((keyboard or board) same video same mouse)) same switch | 1163 |
| <input type="checkbox"/> | L2 | L1 and (clock adj pin) | 1 |
| <input type="checkbox"/> | L1 | kvm same switch | 223 |

END OF SEARCH HISTORY